

# ANALOG WAY MIDRA™ 4K

## Module: INPUTS

### Crestron 3-series & 4-series & VC-4

Date: **December 2022**  
Driver version: **V1.3.0**  
Compatible with: **Midra™ 4K Firmware v2.2.33 or above**

## GENERAL

This module provides information about the inputs of the Midra™ 4K video processor as well as some commands to switch on/off HDCP compatibility.

# CONTROL

## General

<code>AW_In_RefreshInfos</code>	<code>Digital_in</code>	Pulse this signal to force the download and processing of information from the Midra 4K video processor. Most of the time, this signal is never used
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## Inputs\_status

<code>AW_In_Available_FB[X]</code>	<code>Digital_out</code>	Equals 1 when input X is available, 0 otherwise
<code>AW_In_SignalPresence_FB[X]</code>	<code>Digital_out</code>	Equals 1 when a signal is detected on input X, 0 otherwise
<code>AW_In_PlugUsed_FB[X]</code>	<code>Analog_out</code>	The index of the plug used on input X (for multiplug inputs only)

## Hdcp

<code>AW_In_HdcpToggle_Cmd[X]</code>	<code>Digital_In</code>	Toggles HDCP for input X
<code>AW_In_HdcpDefault_Cmd[X]</code>	<code>Digital_In</code>	Forces HDCP default mode for input X
<code>AW_In_Hdcp_FB[X]</code>	<code>Digital_Out</code>	Equals 1 when HDCP is enabled for input X

## In\_Program

<code>AW_In_UsedInProgram_FB[X]</code>	<code>Digital_Out</code>	Equals to 1 when input X is displayed in a Screen or an AUX (Program)
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## In\_Preview

<code>AW_In_UsedInPreview_FB[X]</code>	<code>Digital_Out</code>	Equals to 1 when input X is displayed in a Screen or an AUX (Preview)
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## Format

<code>AW_In_SignalWidth_FB[X]</code>	<code>Analog_Out</code>	The signal width in pixels for input X
<code>AW_In_SignalHeight_FB[X]</code>	<code>Analog_Out</code>	The signal height in pixels for input X
<code>AW_In_Format_FB[X]</code>	<code>String_Out</code>	The signal format for input X (syntax: Width x Height in pixels)

## Label

<code>AW_In_Label_FB[X]</code>	<code>String_Out</code>	The label for Input X as configured through the Web RCS
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